

ELECTRICAL CHARACTERISTICS

| Part No. | Working Voltage (Vw) | Clamping Voltage (Vc) | ESD Withstanding | Capacitance (C) | | Capacitance Tolerance |
|------------------------|----------------------|-----------------------|------------------|-----------------|------|-----------------------|
| | Volts | Volts | Times | pF | | % |
| | <15 μ A | 1A,8/20 μ s | 8KV* | 1KHz | 1MHz | |
| JMV0402C120T560 | 12 | 50 | > 1000 | - | 56 | \pm 20% |
| | | | | | | |
| | | | | | | |

* - In system ESD withstanding pulse per IEC 61000-4-2, 8KV, contact discharge method.

Vw- The max. steady state DC operating voltage of which varistor could maintain also not exceeding 15uA leakage current.

Vc- The peak voltage acrossed the varistor measured at a specified pulse current and waveform.

C - The device capacitance measured with 1.0Vrms, 1KHz / 0.5rms, 1 l

MLV Storage condition \rightarrow Temperature: $\leq 30^{\circ}\text{C}$ / Humidity : $\leq 60\%$ RH (Moisture Sensitivity Levels: 2a)

MLV Preservation period \rightarrow 6 months

External Dimension

Chip Dimension

| Chip Size | inch(mm) | | | |
|----------------|--|--|------------------------|--|
| | L | W | T | A |
| 0402 (1005) | 0.040 \pm 0.004 (1.00 \pm 0.10) | 0.020 \pm 0.004 (0.50 \pm 0.10) | 0.024max. (0.6max.) | 0.010 \pm 0.006 (0.25 \pm 0.15) |

